References

[1] Ben W. Cook, Steven Lanzisera and Kristofer S. J. Pister.(2006), “SoC Issues for RF Smart Dust,” *Proceedings of the IEEE*, *Vol 94, Issue No. 6,* pp. 1177-1196.

[2] M. D. Scott, B. E. Boser, and K. S. J. Pister. (2003), “An ultra low-energy ADC for smart dust,” *IEEE J. Solid-State Circuits*, *Vol. 38, Issue No. 7,* pp. 1123–1129.

[3] Lawrence K. Au, Winston H. Wu, Maxim A. Batalin, Dustin H. McIntire and William J. Kaiser. (2007), “MicroLEAP: Energy-aware Wireless Sensor Platform for Biomedical Sensing Applications,” *IEEE Biomedical Circuits and Systems Conference, 2007. BIOCAS 2007,* pp. 158 – 162.

[4] Bob Verbruggen, Jan Craninckx, Maarten Kuijk, Piet Wambacq, Geert Van der Plas. (2010), “ A 2.6mW 6b 2.2GS/s 4-times Interleaved Fully Dynamic Pipelined ADC in 40nm Digital CMOS,” in *IEEE International Solid-State Circuits Conference (*ISSCC 2010), *Vol. 45, Issue No. 10*, pp. 2080-90.

[5] Ashutosh Verma, Behzad Razavi.(2009), “A 10-Bit 500-MS/s 55-mW CMOS ADC,” in *IEEE J. Solid-State Circuits, Vol 44, No. 11,* pp. 3039-3050.

[6] Robert Payne, Marco Corsi, David Smith, Tien-Ling Hsieh, and Scott Kaylor. (2009), “A 16-Bit 100 to 160 MS/s SiGe BiCMOS Pipelined ADC with 100 dBFS SFDR,” in *IEEE J. Solid-State Circuits, Vol. 45, Issue No. 12, pp. 2613-2622.*

[7] Omid Rajaee, Tawfiq Musah, Nima Maghari, Seiji Takeuchi,Mitsuru Aniya, Koichi Hamashita and Un-Ku Moon(2010), “Design of a 79 dB 80 MHz 8X-OSR Hybrid Delta-Sigma/Pipelined ADC”, in *IEEE J. Solid-State Circuits, Vol 45, Issue No. 4*, pp. 719-730.

[8] Jason Lester Hill.(2003), “System Architecture for Wireless Sensor Networks,” PhD thesis in Computer Science in the graduate division of the University of California, Berkeley.

[9] Shad Roundy, Eli S. Leland, Jessy Baker, Eric Carleton, Elizabeth Reilly, Elaine Lai, Brian Otis, Jan M. Rabaey, V. Sundararajan and Paul K. Wright. (2005), "Improving Power Output for Vibration-Based Energy Scavengers," *IEEE Pervasive Computing, Vol. 4, Issue No. 1,* pp. 28-36.

[10] Shad Roundy, Paul K. Wright and Jan Rabaey.(2003), "A Study of Low Level Vibrations as a Power Source for Wireless Sensor Nodes,"*Computer Communications, Vol. 26, No. 11, 2003,* pp.1131–1144.

[11] B. Calhoun, D. Daly, N. Verma, D. Finchelstein, D. Wentzloff, A. Wang, S. Cho, and A. Chandrakasan. (2005), "Design Considerations for Ultra-Low Energy Wireless Microsensor Nodes," *IEEE Trans. Computers, Vol. 54, Issue No. 6*, pp. 727-740.

[12] Mark Hempstead, Michael J. Lyons, David Brooks, and Gu-Yeon Wei. (2008), "Survey of Hardware Systems for Wireless Sensor Networks," *Journal of Low Power Electronics 2008, Vol. 4, Issue No. 1,*pp 1-10.

[13] Fei Hu and Xiaojun Cao. (2010), " Wireless Sensor Networks: Principles and Practice," : Auerbach Publications.

[14] Walt Kester. (2005), "Which ADC architecture is right for your application," in [online pdf], Available: http://www.techdesignforums.com/practice/technique/which-adc-architecture-is-right-for-your-application/.

[15] Abhisek Dey. (2011), "Design and Implementation of CMOS Low-Power 12bit 100kSPS Successive Approximation Register ADC," Master's of Science thesis in Department of Electronics and Electrical Communication Engineering, Indian Institute of Technology, Kharagpur.

[16] J. H. Nielsen, E. Bruun. (2004), "A low-power 10-bit continuous-time CMOS Sigma-Delta A/D converter," *Circuits and systems, ISCAS '04. Proceedings International Symposium, Vol. 1,* PP. 417-20.

[17] Anuj Agarwal. (2005), "Low-Power Current-Mode ADC for CMOS Sensor IC," Master's of Science thesis in Electrical Engineering in Graduate Studies of Texas A&M University.

[18] N. Verma and A. P. Chandrakasan. (2007), "An ultra low energy 12-bit rate resolution scalable SAR ADC for wireless sensor nodes," *IEEE J. Solid-State Circuits, Vol. 42, Issue no. 6*, pp. 1196–1205.

[19] Devarajan,S.; Singer, L.; Kelly, D.; Decker, S.; Kamath, A.; Wilkins, P. (2009), "A 16b 125MS/s 385mW 78.7dB SNR CMOS pipeline ADC", *Solid-State Circuits Conference -Digest of Technical Papers, ISSCC,* pp. 86-87,87a.

[20] J. Lee, H.-G. Rhew, D. Kipke, and M. Flynn. (2010), " A 64 channel programmable closed-loop neurostimulator with 8 channel neural amplifier and logarithmic ADC," *IEEE J. Solid-State Circuits, Vol. 45, Issue no. 9,* pp. 1935–1945.

[21] Simone Gambini, Jan Rabaey. (2006), "A 1.5MS/s 6-bit ADC with 0.5V supply," *Solid –State Circuits Conference, ASSCC,* PP. 47-50.

[22] Rex Min and Anantha Chandrakasan. (2002), " A Framework for Energy-Scalable Communication in High-Density Wireless Networks," *ISLPED'02, Proceedings of the 2002 International Symposium on low power electronics and design,* PP. 36-41.

[23] Brett Warneke, Bryan Atwood, and Kristofer S. J. Pister. (2001), "Smart Dust Mote Forerunners," *The 14th IEEE International conference on micro electro mechanical systems,* PP. 357-360.

[24] Daniela De Venuto, Eduard Stikvoort and, David Tio Castro. (2010), "Ultra Low-Power 12-bit SAR ADC for RFID Applications," *Design, Automation & Test in Europe Conference & Exhibition*, PP. 1071-1075.

[25] Stephen O'Driscoll and Teresa H. Meng. (2009), "Adaptive Resolution ADC Array for Neural Implant," *31st Annual International Conference of the IEEE, EMBS*, PP. 1053-1056.

[26] Chi-Sheng Lin and Bin-Da Liu. (2003), "A New Successive Approximation Architecture for Low-Power Low-Cost CMOS A/D Converter " *IEEE Journal of solid-state circuits, Vol. 38, No. 1,* PP. 54-62.

[27] Sekendi B. Kobenge and Huazhong Yang. (2009), "A novel low power time mode comparator for successive approximation register ADC," *IEICE Electronics Express, Vol. 6, No. 16*, PP. 1155-1160.

[28] A C. Kailuke, Vrushali G. Nasre, M.Shojaei-Baghini and R.D.Kanphade. (2007), "Design of Low Power Integrated SAR-ADC in 0.18µm Mixed-Mode CMOS Process," *Proceedings of SPIT-IEEE Colloquium and International Conference*, Vol. 2, PP. 62-67.

[29] Hwang-Cherng Chow and Yi-Hung Chen. (2007), "1V 10-bit Successive Approximation ADC for Low Power Biomedical Applications, " 18th *European Conference on Circuit Theory and Design,* PP. 196-199.

[30] L. Doherty,B.A. Warneke, B.E.Boser and K.S.J.Pister. (2001), "Energy and performance considerations for Smartdust," *International Journal of parallel and distributed systems and networks, Vol 4, No. 3*, PP. 121-133.

[31] B.A. Warneke et al.(2002), "An autonomous 16 mm^3 solar powered node for distributed wireless sensor networks", *IEEE Sensors 2002 proceedings, vol. 2*, PP. 1510-1515.